

Notice of Allowability

Application No.

10/729,751

Examiner

Ji H. Bae

Applicant(s)

BONACCIO ET AL.

Art Unit

2115

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment filed on 9 May 2007.
2. ☒ The allowed claim(s) is/are 2-8,10,11,13-19,21,22 and 31-48.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☒ Interview Summary (PTO-413),
Paper No./Mail Date 20070614.
7. ☒ Examiner's Amendment/Comment
8. ☐ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

THOMAS LEE
SUPERVISING PATENT EXAMINER
TECHNOLOGY CENTER 2100

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Anthony M. Palagonia on 15 June 2007.

The application has been amended as follows:

In the claims,

- Claim 2, line 2, inserted after "repairable", -- circuit --;
- Claim 3, line 1, deleted "said pulsed signal is" after "wherein", inserted after "wherein", -- said pulses comprise --;
- Claim 4,
 - line 5, inserted after "one or more", -- repairable --;
 - line 6, inserted after "pre-determined", -- cycle --;
- Claim 8,
 - line 1, inserted after "wherein", -- selected gates of a field programmable gate array are used to implement --;
 - line 2, deleted "is implemented in a field programmable gate array" after "element", inserted after "element", -- , --;
 - line 3, inserted before "selected", -- said --;
- Claim 11,
 - line 6, inserted after "one or more", -- repairable --;
 - line 7, inserted after "pre-determined", -- cycle --;
- Claim 13, line 2, inserted after "repairable", -- circuit --;

Art Unit: 2115

- Claim 15, line 3, deleted “generated” after “pulse”, inserted after “pulse”, -
- generator --;
- Claim 16, line 4, deleted “is” after “repair processor”;
- Claim 19,
 - line 1, inserted after “wherein”, -- selected gates of a field programmable gate array are used to implement --;
 - line 2, deleted “is implemented in a field programmable gate array” after “element”, inserted after “element”, -- , --;
 - line 3, inserted before “selected”, -- said --;
- Claim 22, line 3, deleted “generated” after “pulse”, inserted after “pulse”, -
- generator --;
- Claim 33, line 2, inserted after “repairable”, -- circuit --;
- Claim 34, line 1, deleted “said pulsed signal is” after “wherein”, inserted
after “wherein”, -- said pulses comprise --;
- Claim 38,
 - line 1, inserted after “wherein”, -- selected gates of a field programmable gate array are used to implement --;
 - line 2, deleted “is implemented in a field programmable gate array” after “element”, inserted after “element”, -- , --;
 - line 3, inserted before “selected”, -- said --;
- Duplicate of claim 41, first instance, directed towards the integrated circuit
of claim 11: CANCELLED.
- Claim 43, line 4, deleted “is” after “repair processor”;
- Claim 46,

Art Unit: 2115

- line 1, inserted after "wherein", -- selected gates of a field programmable gate array are used to implement --;
- line 2, deleted "is implemented in a field programmable gate array" after "element", inserted after "element", -- , --;
- line 3, inserted before "selected", -- said --.

The examiner notes that the claim following claim 46 is numbered as claim 48, and that there is no claim 47 present in the application. Since claim 41 (directed towards the integrated circuit of claim 11) was cancelled, the examiner has added new claim 47 to replace the cancelled claim. New claim 47 reads:

The integrated circuit of claim 11, further including:

said cycle counter generating a signal encoding what fraction of said predetermined cycle count is reached; and

said repair processor receiving said signal and repairing said repairable circuit element when said signal encodes a fraction of 1.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ji H. Bae whose telephone number is 571-272-7181.

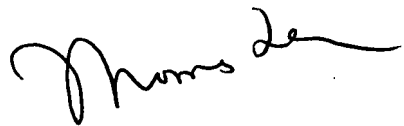
The examiner can normally be reached on Monday-Friday, 10 am to 6:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Thomas Lee can be reached on 571-272-3667. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2115

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Ji H. Bae
Patent Examiner
Art Unit 2115
ji.bae@uspto.gov
571-272-7181



THOMAS J. LEE
SUPERVISOR, ART UNIT 2115
TECHNOLOGY CENTER